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Application Number	10/54051
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First Named Inventor	Takenori OSADA
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Examiner Name	Unassigned
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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
		Feng Zhao et al., "Hall and photoluminescence studies of effects of the thickness of an additional $In_{0.3}Ga_{0.75}As$ layer in the center of $In_{0.15}Ga_{0.85}As/Al_{0.25}Ga_{0.75}As/GaAs$ high electron mobility transistors", <i>Materials Science in Semiconductor Processing</i> , Vol. 5, February 2002, pp. 23-26.	
		U. Strauß et al., "Carrier mobilities in graded $In_xGa_1As/Al_{0.2}Ga_{0.8}As$ quantum wells for high electron mobility transistors", <i>Journal of Applied Physics</i> , Vol. 80, No. 1, July 1, 1996, pp. 322-325.	

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